Area Selective ALD
Room Plaza F - Session AS-SuA

Area Selective Deposition I: Deactivation (1:30-3:30 pm)/Inherent Selectivity, Activation, Deactivation (4:00-5:30 pm)

Moderators: Suvi Haukka, ASM Microchemistry Ltd., Finland, David Thompson, Applied Materials, Erwin Kessels, Eindhoven University of Technology, the Netherlands, Adrie Mackus, Eindhoven University, Netherlands

1:30pm AS-SuA-1 Area Selective Deposition Using Spatial ALD and Polymer Patterns, Carolyn Ellinger, Eastman Kodak Company INVITED

In our laboratory, we use polymers in combination with inorganic metal oxides deposited by spatial atomic layer deposition (Spatial ALD, SALD) in the fabrication of electronic devices and circuits. This presentation will focus on the interaction between Spatial ALD metal oxide film growth and the properties of polymer thin films. We have previously presented on the process space of Spatial ALD and selective area deposition in the context of our “patterned-by-printing” approach to printed electronics. In contrast, this presentation will provide a more general overview of the interaction-space and the resultant patterns and structures (and associated potential applications). Where possible, comparisons will be made to the more common processes of temporal ALD and selective area deposition using self-assembled monolayers (SAMs).

We will begin with a brief overview of SALD and the characteristics that distinguish it from chamber-based temporal ALD. Building on that understanding, we will review the range of potential interactions between polymer thin films and the SALD precursor chemistries (specifically TMA, DEZ, DMAI, and H2O) and process conditions. The conversation will then move to the discussion of useful polymers for selective area deposition – reviewing both specific polymers as well as more generalized polymer properties. A range of patterning techniques and resultant patterns and structures will also be reviewed.

The implementation of SALD used in our laboratory operates at atmospheric pressure conditions, and is capable of coating a wide range of substrates – from rigid wafers and flat panels to flexible substrates that are either singulated or a continuous web. By using polymer deposition techniques (such as printing methods) that are similarly compatible with a broad range of substrates, the patterns and structures reviewed can be translated to nearly any substrate. There is promise for implementing these techniques (or their analogs) in existing manufacturing environments, as well as for using them as a basis for the development of alternative manufacturing lines.

2:00pm AS-SuA-3 Area-selective ALD of Silicon Oxide using Acetylacetone as Inhibitor in a Three-step Cycle, Adrie Mackus, A Mameli, M Merks, B Karasulu, W Kessels, Eindhoven University of Technology, Netherlands

Area-selective ALD is attracting renewed attention in the last few years because of its envisioned application in self-aligned fabrication schemes for future nanoelectronics. In this contribution, a new approach for achieving area-selective ALD will be introduced, based on the use of inhibitor molecules in a three-step (ABC) ALD process. This approach is inspired by previous work in which similar inhibitor molecules were used to enhance the doping efficiency of ALD-grown films by blocking adsorption sites. A area-selective ALD can be obtained using such an ABC ALD process, in the case where the inhibitor molecule selectively adsorbs on specific materials (in step A), and blocks the precursor adsorption in the subsequent pulse (in step B).

Area-selective ALD of SiO2 has been demonstrated for an ALD process consisting of acetylacetone (Hacac) inhibitor (A), H2Si[N(C2H5)2]3 precursor (B), and O2 plasma (C) pulses. This process results in immediate growth with a growth rate of 0.09 nm/cycle on GeO2 and SiO2, and significant nucleation delays on Al2O3, HfO2, and TiO2, as determined from in-situ spectroscopic ellipsometry measurements. The nucleation behavior of this SiO2 process on GeO2 and Al2O3 was corroborated by X-ray photoelectron spectroscopy (XPS). In addition, the selectivity of the ALD growth was demonstrated on samples with patterned Al2O3 on GeO2. Density functional theory (DFT) simulations suggest that Hacac adsorbs readily on Al2O3, while the adsorption on SiO2 itself is an endothermic reaction with a high activation barrier. In-situ Fourier transform infrared spectroscopy (FTIR) studies confirmed that Hacac chemisorbs on Al2O3 and blocks H2Si[N(C2H5)2]3 precursor adsorption. Conversely, only a small amount of Hacac adsorption was detected on SiO2.

A unique feature of this new approach for area-selective ALD is that it distinguishes between the growth on different metal oxide surfaces (e.g., GeO2/SiO2 versus Al2O3/HfO2/TiO2). Moreover, in contrast to most other area-selective ALD approaches, it is compatible with plasma-assisted or oxygen-based ALD. Both of these features open up new application opportunities for area-selective ALD. It is expected that the approach can be extended to other materials, and potentially allows for tuning the selectivity by selecting the inhibitor molecule.


2:15pm AS-SuA-4 Evaluation of Different Nanoimprint Resists for a use in Area-selective Atomic Layer Deposition of Selected Materials, R Ritasalo, Picosun Oy, Finland; M Messerschmidt, Micro Resist Technology GmbH; K Grigoras, VTT, Finland; S Ek, Picosun Oy, Finland; E Østreg, Picosun Oy; M Prunnila, VTT; Tero Pivi, Picosun Oy

Nano-patterns of diverse materials are of much interest and have great relevance as a means of enhancing performance, enabling miniaturization, or specific components of functional devices or interfaces. Several top-down and bottom-up techniques have been attempted to meet these goals, however with varying degrees of success [1]. In this study we employ a combination of nanoimprint lithography (NIL) and area-selective atomic-layer deposition (ALD); in which NIL is used to produce suitable patterned polymeric templates for area-selective ALD enabling thus a patterned ALD growth.

ALD requires typically the presence of OH-groups on the sample surface in order to get growth initiated. The lack of active surface sites e.g. in the case of hydrophobic polymer layers inhibits the nucleation process. This method would enable growth of patterned ALD wafers and would therefore eliminate one costly mask layer step in the fabrication process.

The main goal of this study is to evaluate and to identify those NIL resists that feature effective ALD growth inhibition properties and to test the concept with NIL-patterned templates and using PICOSUN™ ALD technology. For these investigations in total four NIL resists with different chemical compositions were chosen. The selection of ALD materials included oxides of ZnO, HfO2, ZrO2, Ta2O5, TiO2 and Al2O3, as well as Pt as a metallic candidate. After the ALD step the samples were treated by oxygen plasma to evaluate their performance in terms of the area selectivity. Examples of this empirical approach are presented in Fig. 1. In those cases in which a compact ALD coating layer is formed onto the organic NIL resist, the oxide layer acts as an etch barrier for the oxygen plasma, so that no NIL resist is removed (Fig. 1b,c) while in those cases where no compact oxide layer is deposited, the oxygen plasma will attack and remove the organic NIL resist (Fig. 1a).

The results revealed that the type of applied NIL resist, the type of investigated ALD material and also the applied ALD parameters had all significant effects on the ALD growth inhibition capability. Usually the selectivity was few tens of cycles but at best it reached over 200 cycles. The most promising candidates for a use in area-selective materials by ALD were ZnO, HfO2, ZrO2 and one of the two studied TiO2 and Al2O3 processes. We also found, that in such cases where only a limited area-selectivity of the NIL resist toward the ALD process is observed, nanopatterns can also be effectively fabricated (Fig. 2).


Acknowledgements: The research has received funding from the EU FP7 project QUANTIMETH (604668)

2:30pm AS-SuA-5 Developing a Full Wafer-scale Approach Towards High ALD Selectivity on Copper vs Low-k (and Oxides) using a Single ALD/SAMS Platform, Laurent Leclerc, Ultratech; S Armini, S Herregods, IMEC, Belgium

Implementation of robust production-scalable methodologies for area-selective deposition (ASD) could have a significant impact on simplifying process flows during device fabrication. However process integration at the wafer scale (i.e., 200-300 mm diam.), including surface pre-treatment, surface de-activation followed by selective ALD has remained elusive. In this work, we present our results towards achieving excellent selectivity of HfN, ALD on copper vs. low-k (and oxides) using C18 octadecanethiol (ODT) as a blocking layer.

SAMs and ALD depositions were performed in a commercial Ultratech CNT Savannah platform equipped with a SAMs kit optimized for delivery of large C18 molecules in gas phase. In-situ spectroscopic ellipsometry (SE)
was used to optimize the process performance during the different steps including reduction of the native copper oxide, ODT passivation and selective ALD growth. Results were validated with ex-situ XPS, RBS and SEM characterization.

Several methods were investigated to rapidly reduce the native copper oxide including exposures to ethanol as well as TMA and TDMAT. ALD-inhibiting films were deposited in gas phase with short (<900s) ODT exposures. Temperature and reduction methods were shown to have a significant impact on ODT growth varying from saturating single monolayer growth to non-self limited growth with thicknesses varying from 30 to 80Å. In-situ SE measurements during HFN, ALD on ODT-treated copper and low-K showed a high degree of selectivity with HFN growth delayed for up to 480 ALD cycles on copper, while no growth inhibition was detected on ODT-treated low-K and oxides. Interestingly the non-self limited ODT films were shown to be the best ALD-growth inhibitors.

The authors will discuss the benefits and challenges associated with this rapid film phase methodology, emphasizing process time and wafer scalability to 300 mm, dose control challenges associated with delivery of large C18 SAMS molecules, and the significant impact of self-limited vs. non self-limited ODT growth on ASD performances.

2:45pm AS-Sua-6 Fabrication of Large-area Nanolines by Area-selective Atomic Layer Deposition, J Ekerdt, Zizhuo Zhang, University of Texas at Austin

Metal lines are common in semiconductor applications, such as interconnect lines and advanced memory word/bit lines. As the device features scale down, fabrication of large-area uniform nanolines becomes harder. Photolithography followed by etching is the usual patterning method for nanolines but the 100-nm pitch has introduced lithographic challenges when applied across large areas. Furthermore, subtractive etching of metal films typically relies on plasma processes that can generate side products detrimental to device performance.

We offer an alternative to patterning large-area nanolines through a sequence of nanoimprint-lithography, area-selective atomic layer deposition (A-SALD) of a metal oxide film and oxide reduction to the metal. The nanoimprint process can create wafer-scale nanoscale patterns with low cost, high throughput and high resolution. A-SALD is a process by which the chemical nature/reactivity of a surface can be manipulated such that ALD nucleation occurs only in desired regions.

In this work, Co is taken as the exemplary metal; however the process works for other transition metals, including Fe and Ni. Previous efforts to deposit Co metal using bis(N-tert butyl, N’ethylpropionamidato) cobalt (II) and H2 in ALD have suffered from carbon and nitrogen incorporation into the film and nucleation on polymer- and alkyllamine-coated surfaces. Therefore a route to impurity-free CoO deposition in the defined areas followed by reduction from CoO to Co is selected. The approach is illustrated with a 110-nm pitch nanoline pattern of 40 nm-wide trenches and 120 nm-high cored-imprint polymer lines created by nanoimprinting. After O2/Ar plasma-etching to remove a 10 nm residual layer at the trench bottom, CoO ALD occurs only at the bottom of the trenches. Following deposition at 180 °C and removal of the polymer lines with etching, the CoO can be reduced to form Co metal using a reducing gas at elevated temperature or an oxygen-scavenger layer, such as Al, near ambient temperature. With this A-SALD approach, we are able to pattern large-area uniform and parallel Co metal nanolines with a 110-nm pitch. The nanoline width can be tuned by the O2/Ar treatment of imprinted pattern and the height of nanolines can be adjusted by the number of ALD cycles. X-ray photoelectron spectroscopy is used to determine the composition and oxidation state of nanolines at various steps in the process. Scanning electron microscope and atomic force microscope are used to determine the pitch, width and height of the nanolines. Using I-V measurement and vibrating sample magnetometer, we explore the electrical and magnetic properties of the Co nanolines.

3:00pm AS-Sua-7 Nanoscale Selective Deposition of TiO2 using e-beam Patterned Polymeric Inhibition Layers and TDMAT Precursor, A Haider, M Yilmaz, P Deminskyi, Bilkent University, Turkey; Hamit Eren, Delft University of Technology, Netherlands; N Biyikli, Utah State University

Controlling the lateral dimensions of thin films by patterning is an essential requirement for microelectronics industry towards continuous device miniaturization. Conventionally, thin film patterning is achieved by photolithography which includes several processing steps. During the atomic layer deposition (ALD) process, film nucleation is critically dependent on the surface chemistry of the substrate which makes it possible to achieve area-selective ALD (AS-ALD) by chemically modifying the substrate surface. Local modification of substrate surface opens up possibilities to achieve lateral control over film growth in addition to robust thickness control during ALD process. AS-ALD offers numerous advantages in device fabrication such as reduction of the lithography steps required, elimination of complicated etching processes, and minimization of expensive and poisonous reagent use.

In this work, we report a detailed investigation to select the most compatible polymer blocking layer among poly(methyl methacrylate) (PMMA), polyvinylpyrrolidone (PVP), and inductively coupled plasma (ICP) polymerized fluorocarbon (CF) for AS-ALD of TiO2. TiO2 was grown at 150 °C using tetraakis(dimethylamido) titanium (TDMAT) and H2O as titanium and oxygen precursors, respectively. PMMA and PVP were deposited using spin coating and C6F8 was grown using inductively coupled plasma (ICP) etch reactor. Contact angle, scanning electron microscope (SEM), spectroscopic ellipsometer, and X-ray photoelectron spectroscopy (XPS) measurements were performed to investigate the effectiveness of polymer layers for AS-ALD process of TiO2. TiO2 was grown with different number of growth cycles (maximum = 1200 cycles) on PMMA, PVP and CF. PMMA revealed successful growth inhibition up to the maximum inspected growth cycles. PVP was able to block TiO2 growth up to 300 growth cycles, while CF revealed no inhibition capability. Initially, mm, um, and nm-scale self-aligned deposition of TiO2 is demonstrated using a PMMA masking layer that has been patterned using e-beam lithography. Additionally, we used the selectively grown TiO2 layers as an etch mask layer to create deep trench structures inside Si. SEM, EDX line scan, EDX elemental mapping, and XPS elemental mapping measurements revealed successful patterning of TiO2 features. AS-ALD of TiO2 demonstrated in the present work offers a novel approach to fabricate close packed nanopatterns for various device architectures without any complex etching or lift-off processes.

3:15pm AS-Sua-8 Area-selective Atomic Layer Deposition using Inductively Coupled Plasma Polymerized Fluorocarbon Layer: A Case Study for Metal-Oxides and Metals, Ali Haider, P Deminskyi, T Khan, Bilkent University, Turkey; H Eren, Delft University of Technology, Netherlands; N Biyikli, Utah State University

Nanoscale process integration demands novel nano-patterning techniques in compliance with the requirements of next generation devices. Conventionally, top-down subtractive (etch) or additive (deposition/lift-off) processes in conjunction with various lithography techniques is employed to achieve film patterning, which become increasingly challenging due to the ever-shrinking misalignment requirements. To reduce the complexity burden of lithographic alignment in critical fabrication steps, self-aligned processes such as selective deposition and selective etching might provide attractive solutions.

Here, we demonstrate a methodology to achieve area-selective ALD (AS-ALD) by using inductively couple plasma (ICP) grown fluorocarbonpolymer film as growth inhibition layer. The fluorocarbon layer was grown using C6F8 feed gas in a conventional ICP-etch reactor. Our approach has been tested for metal-oxides and metals including ZnO, Al2O3, HfO2, Pt, and Pd. Contact angle, X-ray photoelectron spectroscopy, spectroscopic ellipsometer, and scanning electron microscopy measurements were performed to investigate the blocking ability of plasma polymerized fluorocarbon layers against ALD-grown films. Characterizations carried out revealed that effective blocking on fluorocarbon layer is achieved for ZnO, Pt, and Pd films with different blocking cycle numbers before nucleation initiates. On the other hand, a rather slow nucleation has been observed for HfO2 growth on fluorocarbon coated surfaces, while Al2O3 growth showed almost no delay with a growth rate equal to the ones on conventional substrate surfaces. By exploiting this inhibition feature, thin film patterning has been demonstrated by growing ZnO, Pt, and Pd films on photolithographically patterned fluorocarbon/Si samples. The robust albeit rather simple and straightforward technique presented in this work overcomes various challenges associated with previous methods of AS-ALD and provides an alternative route towards nano-patterning using selective deposition.

4:00pm AS-Sua-11 Adventures and Advances in Selective Deposition, David Thompson, Applied Materials

Please submit your abstract to Della Miller, della@avs.org, immediately for inclusion in the program.
Area-Selective Atomic Layer Deposition (AS-ALD) can push device manufacturing towards new frontiers. To date, several approaches have been developed for achieving area-selective deposition that can be classified as either area-activation or area-deactivation. The former is referred to as direct-write ALD whereas area-selective steps are required. In this work, we report on direct-write ALD of two transparent conductive oxides, In$_2$O$_3$:H and ZnO. The method exploits the selective precursor adsorption on OH-terminated versus H-terminated Si surfaces and the resulting differences in nucleation delay.

Previously we have demonstrated that microscale In$_2$O$_3$:H patterns can be fabricated by local activation of the surface using a micro-plasma printer, followed by a second step in which AS-ALD takes place on the activated surface only. Similar chemistry is now explored for ZnO AS-ALD, while aiming at nanoscale dimensions. In this case an ultra-thin SiO$_2$ seed layer was deposited to activate the H-terminated surface (i.e., a-Si:H) for ALD growth. 500-by-500 nm$^2$ SiO$_2$ seed layers were deposited by electron beam induced deposition (EBID) using tetraethyl orthosilicate (TEOS) and H$_2$O as the precursors. For the ALD of In$_2$O$_3$, diethylzinc (DEZ) and H$_2$O were used as the precursor and the co-reactant, respectively, in a thermal ALD process. In substrate temperatures ranging from 100 to 250$^\circ$C, the selectivity of deposition of ZnO was obtained upon 80 ALD cycles, with good selectivity as demonstrated by in-situ ellipsometry, SEM, cross-sectional TEM and energy dispersive X-ray spectroscopy (EDX) analysis. The influence of the ALD process temperature and EBID patterning parameters on the extent of selectivity have been investigated in detail. First principle density functional theory calculations corroborate the selectivity of the ALD process by revealing a kinetically hindered surface reaction between DEZ and H-terminated Si surfaces.

References

4:45pm AS-Sua-14 Inherent Substrate-Selective Growth of Cobalt and Nickel Metal Films by Atomic Layer Deposition, Marissa M. Kerrigan, C Winter, Wayne State University

Atomic layer deposition (ALD) affords thin films with high conformity and precise thickness control due to its self-limited growth mechanism, and enables the increasing demands for smaller feature sizes in microelectronics devices to be met. Area-selective ALD (ASALD) growth is receiving attention in the fabrication of microelectronic devices, since it can eliminate complicated etching steps from the fabrication process and minimizes expensive and toxic reagent use. To date, few ASALD processes for metals have been reported, driving a need for new processes to be developed. Thin films of cobalt metal grown by ASALD are of considerable interest for use in microelectronics devices, specifically as a metallization liner and cap for copper features in sub-10 nm logic nodes. Our laboratory has recently reported an ALD process for the inherent selective deposition of cobalt metal thin films using bis[1,4-di-tert-butyl-1,3-diaza dibutadieny]cobalt(II), Co(CH$_3$)$_3$(DAD)$_2$, and formic acid. This process affords high-purity, low-resistivity cobalt films, with a growth rate of 0.95 Å/cycle. Importantly, growth occurs preferentially on metal substrates over dielectric substrates. However, the use of formic acid as a co-reactant is undesirable for industrial applications. In this work, we will describe the development of a new ASALD process for high-purity, low-resistivity (~13 µΩcm) cobalt metal films from Co(CH$_3$)$_3$(DAD)$_2$ and alkyl amines, with a growth rate of 0.98 Å/cycle on metal substrates. No cobalt metal growth is observed on dielectric substrates. We will also report a new ASALD process for high-purity, low-resistivity (~20 µΩcm) nickel metal films from Ni(CH$_3$)$_3$(DAD)$_2$ and alkylamines, with a GR of 0.60 Å/cycle on metal substrates. No nickel metal growth is observed on dielectric substrates. We will also discuss extending M(CH$_3$)$_3$(DAD)$_2$ and alkyl amines as a general process for the low temperature ALD growth of other first row transition metal films.


5:00pm AS-Sua-15 Delayed Nucleation of HfO$_2$ and TiO$_2$ ALD on Carbon via Cyclic Plasma Treatments for Application in Selective-Area Deposition, Eric Stevens, North Carolina State University, USA; Y. Tomczak, B. Chan, E. Altamirano-Sanchez, IMEC, Belgium; G. Parsons, North Carolina State University, USA; A Delabie, IMEC, Belgium

Fabrication of future nanoelectronic devices will require innovative and scalable patterning techniques. The use of a sacrificial patterning layer, such as Advanced Patterning Film (APF), would allow metal oxide materials to be deposited in a trench-fill approach, assuming growth inhibition on APF selective to the growth substrate. This work seeks an in-depth understanding of the nucleation and growth behavior of metal oxide ALD on APF, and the effects of surface pre-treatments on the surface functionalization for use in TiO$_2$ and HfO$_2$ patterning.

A high density APF film was deposited on 300mm Si wafers using plasma enhanced CVD of C:H at 400$^\circ$C, yielding an amorphous carbon composed of mostly sp$^2$ and sp$^3$ hybridized carbon. The inherent selectivity of the as-deposited APF films was probed by depositing HfO$_2$ (HfCl$_4$+ H$_2$O at 300$^\circ$C) and TiO$_2$ (TiCl$_4$+ H$_2$O at 300$^\circ$C) on the APF, and ALD processes are anticipated to proceed via island growth on C-C and C-H surfaces. Interestingly, uninhibited ALD growth of both TiO$_2$ and HfO$_2$ was observed for as-deposited APF substrates, presumably due to <5% oxygen present at the surface (X-ray Photoelectron Spectroscopy, XPS) and an RMS value of 0.55 nm.

In order to remove oxygen and impede nucleation of metal oxide ALD, an in situ, direct H$_2$ plasma pre-treatment was investigated. XPS depth profiling revealed that for both treated and untreated APF, the oxygen content converged to ~1% after 2-3 nm Ar sputtering, implying a partial oxidation of the APF surface when exposed to air. A plasma pretreatment of 100W for 10s was sufficient to reduce the surface oxygen from 5% to 3%, with ~3 nm of APF etching observed (spectroscopic ellipsometry). The combination of high power and cyclic treatments are believed to etch the partially oxidized top few nanometers and reduce the amount of sites available for re-oxidation, resulting in mostly C-H and C-C surface termination.

The effect of plasma power was screened by subjecting APF substrates to in situ plasma treatments followed by 30 cycles of TiO$_2$ ALD (Figure 1). Low power treatments (e.g., 50W) had minimal effect, whereas using 400W and repeated cycling within the ALD sequence (e.g., [400Wx2 + 150Cx2]) yielded growth below the RBS detection limit on the APF, corresponding to 1.3 nm of TiO$_2$ on receptive silicon nitride. Furthermore, a cyclic H$_2$ plasma treatment of 400Wx2 allowed for up to 60 cycles of TiO$_2$ and HfO$_2$ ALD on APF without film closure (Figures 2, 3). These insights can aid the design of patterned substrates which incorporate area selective ALD of TiO$_2$ and HfO$_2$ on SiN, while delaying film closure on APF.

5:15pm AS-Sua-16 Selective Deposition Process Combining PEALD and ALD, Christophe Vallée, Univ. Grenoble Alpes, CNRS, France; R Gassilloud, CEA-Leti, France; R Vallat, Univ. Grenoble Alpes, CNRS, France; C Mannequin, University of Tsukuba; A Uedono, University of Tsukuba, Japan; V Pesce, Univ. Grenoble Alpes, CNRS, France; N Possemie, CEA-Leti, France; P Gonon, A Bsiesy, Univ. Grenoble Alpes, France

INVITED

At advanced nodes, lithography starts to dominate the CMOS technology cost and alternative approaches are needed to continue 2D scaling and extend Moore’s law. Selective deposition process, is a very promising path to enhance patterning capabilities and control its cost. From all the different deposition processes, Atomic Layer Deposition (ALD) is probably the more suitable tool to develop a selective deposition due to its high surface sensitivity. The first part of this paper will be an overview of different existing ALD selective deposition processes:

- Selective ALD using self-assembled monolayers (Selective-Area or Area-Selective ALD)
- Selective ALD using block copolymer
- Selective ALD using temperature
- Selective ALD using spatial ALD (Atomic Layer Printer)

In the second part, we will focus on plasma-based selective ALD process. Cold plasmas used for ALD assistance contain many energetic species such
as ions and radicals that can be used to design a selective deposition process. For example, it has been proposed to use an ion implantation assistance to perform topography selective deposition on 3D structures. In our group, we are developing a plasma ALD selective deposition process by adding a plasma etching step. For this process, easy to use and compatible with high throughput manufacturing, three waypoints are needed:

- First a different nucleation delay between two substrates. Here we take advantage of the inherent substrate-dependent growth initiation of ALD;
- Second an etching step is needed with an etch rate controlled at the nm scale in order to remove the deposited material on one substrate;
- A plasma-induced surface modification of the substrate where the material has been removed is needed. Idea here is to add after each etching step an additional nucleation delay before starting the growth again.

This process that has been developed in a 300 mm plasma deposition tool from Kobus will be more detailed and examples of selective deposition processes combining PEALD and etching steps for Ta₂O₅ and TiO₂ will be given. The impact of the process on the chemical and electrical properties of the oxides will also be discussed thanks to XPS, XRR, ellipsometry and positron annihilation spectroscopy as well as C(V) and I(V) characterizations. Enhancing the performance of this process needs to optimize the etching step. For this purpose, we are currently trying to develop specific atomic layer etching processes in the PEALD tool by adding a RF substrate bias waveform developed by Oxford in a FLEXAL tool. This specific experimental set up and first results will also be addressed.
Area Selective ALD Room Plaza Exhibit - Session AS-MoP
Area Selective ALD Poster Session
AS-MoP-1 Area-selective ALD using Vapor and Solution-Phase Synthesized Perfluorodecylchlorosilane (FDT5) SAMs as Growth Inhibition Layers, Ali Haider, Bilkent University, Turkey; S Altuntas, TOBB University of Economics & Technology, Turkey; P Deminsiki, T Khan, Bilkent University, Turkey; F Buyukserin, TOBB University of Economics & Technology, Turkey; N Biyikli, Utah State University
One of the main type of organic materials used for growth inhibition in area-deactivated selective atomic layer deposition (AS-ALD) is self-assembled monolayers (SAMs). SAMs are made up of long organic molecules consisting of three elements: the head group, chain as the backbone, and a tail group. The tail groups of SAM molecules transforms the surface from an OH-terminated hydrophilic surface into a chemically saturated C-H$_2$ or C-F$_2$ hydrophobic surface. When compared with the conventional C-H terminated SAM chemistries, an organic back bone chain made up of fluorine (CF$_2$) and CF$_3$ tail groups might provide an enhanced hydrophobic character and potentially may result in an improved film-growth inhibition performance with higher nucleation initiation threshold ALD-cycle numbers. Moreover, the vast majority of the reported SAM-based AS-ALD work in literature utilizes SAMs which are synthesized in wet-solution phase to modify the substrate surface. Such wet processing features low-compatibility with main-stream device fabrication technology and suffers from long processing times and unavoidable defect sites leading to imperfections in growth inhibition. Vapor phase grown SAMs for AS-ALD could provide additional advantages such as faster processing, eliminating wet processing, topographical selectivity, and facilitation integration with ALD reactors and other fabrication equipments.

In this work, we report an experimental study for AS-ALD using perfluorodecylchlorosilane (FDT5) SAM molecules, which are deposited on Si(100) substrates using both vapor-phase and solution-phase synthesis approaches. A comparison between vapor and solution-phase grown SAMs will be provided in terms of growth inhibition performance. FDT5 monolayer terminated surfaces formed via vapor and solution phase synthesis methods demonstrated exhibit both high-degree of hydrophobicity (contact angle ~120°) owing to long CF$_3$ organic back bone chain and CF$_3$ tail groups. Contact angle, scanning electron microscope (SEM), spectroscopic ellipsometer, and X-ray photoelectron spectroscopy (XPS) measurements results will be presented to demonstrate the effectiveness of FDT5 molecules against thin film growth blockage. Finally, we will present micro-scale patterning of thin films using a photolithography patterned SAMs substrate.

AS-MoP-2 Nanoscale Patterning of C$_{6}$F$_{5}$ Plasma Polymer Blocking Layers via Fiber-Recycled Pulsed Laser Processing for Selective Deposition of Noble Metals, Petro Deminskyi, I Pavlov, S Idyay, O Tokel, Bilkent University, Turkey; H Eren, Delft University of Technology, Netherlands; A Haider, F Idyay, Bilkent University, Turkey; N Biyikli, Utah State University
Area-selective ALD (AS-ALD) is a promising technique for low-temperature self-aligned nanoscale device fabrication by reducing or eliminating lithography/etch process steps. Several methods have been reported for the patterning of ALD-grown films: (1) patterning based on lithography and lift-off; (2) AS-ALD by area-deactivation; (3) AS-ALD by area-activation; and (4) the ideal but highly challenging inherently selective AS-ALD. Though numerous of techniques of surface patterning for AS-ALD have been reported (optical/e-beam lithography), cost-effective alternative solutions are highly attractive.

In this study, we show that by using the pulsed-laser processing technique it is possible to achieve nanoscale patterned surfaces for AS-ALD of noble metals. To develop this alternative methodology, we have used: an ultrashort Yb-fibre laser, operating at a central wavelength of 1030 nm which has a 170-fs pulse duration, 1 MHz repetition rate with adjustable laser power levels. For Pt ALD growth, we used trimethyl (methylcyclopentadienyl) platinum (IV) and ozone as Pt and O$_2$ precursors, with N$_2$ as the carrier gas. Samples have been characterized using HR-SEM, contact angle measurements, EDX-analysis, and XPS for elemental composition analysis.

Our study consists of the following steps:

- C$_{6}$F$_{5}$ plasma polymerization on Si(100) substrate surface in an ICP etch reactor;
- Micro- and nanoscale patterning of C$_{6}$F$_{5}$ plasma polymer by femtosecond (fs) laser pulses;
- Si surface patterning by nonlinear laser lithography (NLL);
- AS-ALD of Pt thin films on patterned C$_{6}$F$_{5}$/Si interface;
- Plasma polymerization of C$_{6}$F$_{5}$ on Pt thin film;
- Hydrophobicity study of dielectric/metal/substrate stack.

We’ll present our experimental results on surface patterning via fs laser pulses and NLL for Pt AS-ALD. Here we speculate about the transparency of C$_{6}$F$_{5}$ polymer to ~ 1 µm wavelength. In this regardsTherefore, due to substrate heating, polymer can be removed from specific areas. Patterned structures with diameters as small as 250 nm have been achieved.

This new capability could pave the way for cost-effective lithography-free patterning technique. For future work, (1) surface patterning with smallest possible features; (2) super-hydrophobic surfaces with metal contacts underneath are under development of which the latest results will be presented as well.

AS-MoP-3 Feasibility Study of Single and Multi-layered Graphene as Plasma-compatible Deactivation Layers for Selective Deposition of III-Nitride Materials, Petro Deminskyi, E Kovalnska, A Haider, C Kocabas, Bilkent University, Turkey; N Biyikli, Utah State University
Plasma-assisted atomic layer deposition (PA-ALD) is a promising method for low-temperature growth of III-nitride materials. However, selective film deposition using PA-ALD is quite challenging mainly due to the plasma-incompatibility of conventional deactivation/blocking layers including SAMs and polymers. The main motivation of this work was to explore alternative growth inhibition materials which could withstand plasma environment.

Towards this goal, we investigated single-layer graphene (SLG) and multi-layered graphene (MLG) as an effective lift-off mask for AlN, GaN, and InN grown via low-temperature PA-ALD. We achieved crystalline III-nitride thin films on graphene blocking layer and Si substrate surface. As far as SLG and MLG possess relatively weak Van der Waals forces between (1) graphene/substrate and (2) graphene/graphene interfaces, here, we gauge the importance of those forces for selective deposition of III-nitride materials. This strategy could be mainly used as an alternative patterning approach based on the graphene-assisted lift-off technique.

To evaluate the selective deposition studies, GaN, AlN, InN, and graphene have been removed from Si surface and were characterized using SEM and XPS. Spectroscopic ellipsometry measurements were performed to measure the film thickness on Si substrate surface and graphene-masked area. An extensive comparative study has been carried out with some successful results for certain binary III-nitride alloys. Possible blocking, nucleation, and graphene-penetration mechanisms will be discussed in conjunction with possible future strategies to further develop selective deposition methods for plasma-assisted film growth recipes.

AS-MoP-4 Electroless Noble Metal Deposition - A New Approach for Highly Selective Surface Controlled Deposition Process, Stanko Brankovic, University of Houston; D Solanki, D Wu, University of Houston, Y Dordi, A Joj, Lam Research
The improved understanding of nucleation kinetics has led to various discoveries in which the thin film growth was manipulated to enhance the evolution of atomically flat epitaxial overlayers. Exploiting some of these results has led to invention of several new methods and protocols for electrodeposition where underpotentially deposited (UPD) monolayer (ML) is used as a mediator, surfactant or sacrificial template. The one successful example is so called “Deposition via Surface Limited Redox Replacement (SLRR) of UPD ML” which gained a lot of applications for synthesis of noble metal thin films with different functionalities. This protocol/method represents the combination between the potential controlled step – formation of UPD ML and electroless step – SLRR of UPD ML by more noble metal ions (galvanic displacement). However, our studies show that there is still a lot of room left for improvements and further simplifications. They should expand application of this deposition protocol in many areas where traditional ALD process has been used.

In this talk we present results demonstrating electroless (e-less) ALD process where deposited Pb monolayer is used as a reducing agent and sacrificial material in SLRR reaction with noble metal ions such as Pt, Pd, Ru etc.... The full deposition cycle involves sequential exposure of the substrate to the solution for Pb ML deposition and then to solution for SLRR reaction and noble metal deposition. This results in an overall
deposition of controlled amount of noble metal which is the function of the areal density of deposited Pb monolayer and stoichiometry of the SLRR reaction. Therefore, the process mimics to great extend the standard ALD cycle where adsorption of the metal precursors and surface catalyzed reaction are replaced by e-less Pb monolayer deposition and SLRR reaction. If two-step SLRR cycle is repeated an arbitrary number of times a highly compact, smooth and conformal noble metal thin film is grown. The deposition process is highly selective to the metal substrates at which Pb forms an UPD monolayer providing an advantage when certain integration requirements are considered. The process is designed (but not limited) for aqueous solutions with fairly simple and stable chemistry that can be easily scaled up to any size and shape of the substrate surface. Results demonstrating details and underlying phenomena controlling this process will be discussed. In addition, an example of a high quality of Pt, Pd and Ru films grown on Cu substrate will be shown as well as the applications of this process for metallization of structures relevant to semiconductor device fabrication.

AS-MoP-5 Inherently Selective Plasma-assisted ALD: A Feasibility Study for III-Nitride Materials, Necmi Biyikli, Utah State University; A Haider, P Deminskyi, Bilkent University, Turkey

ALD processes featuring inherent selectivity without using any area deactivation/blocking layers or area-activation/patterned seed layers, is an almost unexplored field, but is a highly challenging method with enormous potential to revolutionize the nanoscale processing technology. As ALD processes strongly depend on the chemistry of substrate surfaces and precursor molecules, there is significant potential for the development of selective chemistries and reactions possessing large kinetic barriers. Selective atomic layer deposition/epitaxy of nitride materials has not yet been reported mainly due to the plasma-incompatibility of utilized blocking layers. Likewise, inherently selective atomic layer etching is still challenging with there being very limited success and no reports yet on nitrides.

In this work, we aim to develop inherently selective PA-ALD recipes for the nitride material family. To realize this objective, individual deposition reactions featuring selective chemistry with large kinetic barriers for III-nitride materials against major surfaces will be investigated. This unique bottom-up fabrication tool-box for nitride materials would enable the self-aligned fabrication of critical nanoscale structures minimizing the need for expensive and complex lithography-based top-down processing. Current state-of-the-art selective atomic layer processes include selective-area deposition of oxides and metals only, by using various blocking layers which need to be removed afterwards via additional process steps. Our strategic goal is to achieve the major breakthroughs necessary to enable not only selective ALD for III-nitrides, but also the making of this selectivity inherent, i.e., chemically favorable, to eliminate the use of blocking layers and extra processing steps. The highly-challenging task of developing inherently selective nitride-ALD will be embraced through a systematic study featuring extensive materials characterization efforts to gain a full understanding of the material growth dynamics on various surfaces and carefully monitoring the self-limiting surface reactions.

Our initial investigation on the influence of several critical parameters which might lead to the development of inherently selective plasma-assisted surface reactions show that the choice of substrate material and reactor pressure have a considerable impact on the properties of the deposited film. The key achievement sought in our studies is to obtain a certain nucleation delay between different surfaces including metallic (Pt, Cu), oxide (SiO₂, Al₂O₃), and conventional substrates (Si). We would like to share our critical experimental findings throughout the presentation.
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